CERTIFICATE OF MAILING (37 CFR 1.8a)

I hereby certify a problem (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Box Non-Fee Amendment, Washington, D.C. 20231.

Art Unit: Not Yet Assigned

Examiner: Not Yet Assigned

(Signature of person mailing paper)

YAMAP0777US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Cairns, et al

Serial No:

09/943.535

Filed:

August 30, 2001

For:

DRIVING ARRANGEMENTS FOR ACTIVE MATRIX LCDS

Assistant Commissioner for Patents Box Non-Fee Amendment Washington, D.C. 20231

Sir:

This paper is filed in order to correct certain typographical errors in one drawing figure of the above-identified application. Entry of the present paper is respectfully requested.

echnology Center 2000

REMARKS

The presently submitted drawing sheet, Figure 18, contains proposed corrections of the reference numbers. The Figure 18 shown in the originally filed drawing sheet omitted and contained erroneous reference numbers, as a result of typographical errors. Applicants respectfully request entry of the corrected drawing sheet submitted herewith, and submit that such corrections do not constitute new matter.

The requested drawing changes are supported in the specification as filed. Figure 18 is similar to Figure 17, as indicated in the Brief Description of the Drawings at page 13 of the specification. Figure 18 includes the elements of Figure 17, and also includes the additional XOR logic gates 28, mentioned at page 13, lines 14-16 and described at page 26, lines 13-15. In the originally submitted Figure 18, these additional XOR logic gates 28 were not identified by reference numerals. In addition, in the originally submitted Figure 18, the XOR logic gate 126 shown in Figure 17 was erroneously identified by reference numeral 28.

Thus, the presently submitted proposed drawing corrections propose (a) to insert the reference numeral 28 to identify the additional XOR logic gates which were added to the embodiment shown in Figure 17 to provide the embodiment shown in Figure 18; and (b) to correct the reference numeral identification of the single XOR logic gate 126 from the erroneously applied reference numeral 28.

Based on the foregoing, Applicants respectfully submit that the presently submitted drawing corrections contain no new matter and should be entered.

Should the Examiner consider that a telephone interview would be helpful to facilitate favorable prosecution of the above-identified application, the Examiner is invited to contact the undersigned at the telephone number provided below.

No fees are believed due as a result of the filing of this paper. However, should any fees be due therefor, the Commissioner is authorized to charge such fees to deposit account no. 16-0988, docket no. YAMAP0777US.

Respectfully submitted,

RENNER, OTTO, BOISSELLE & SKLAR, P.L.L.

Βv

Neil A. DuChez Reg. No. 26,725

1621 Euclid Avenue Nineteenth Floor Cleveland, Ohio 44115 (216) 621-1113

C:\MyFiles\YAMA\777\YAMA777.drawcorr.wpd



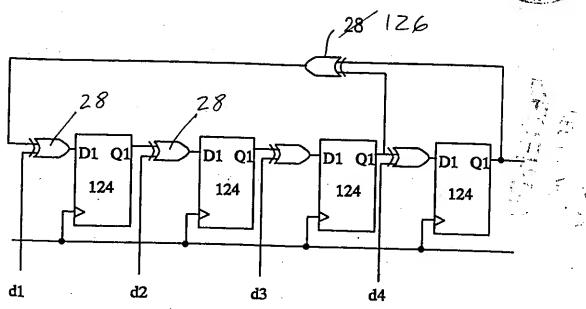


Figure 18